

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/538,216 Confirmation No. 1792  
Applicant : SCHMITZ, Jurriaan  
Filed : June 9, 2005  
TC/A.U. : 2824  
Examiner : BERNSTEIN, Allison

Docket No. : NL02 1416 US  
Customer No. : 65913

Title: : Vertical Insulated Gate Transistor and Manufacturing Method  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE & AMENDMENT**

Sir:

In response to the Non-Final Office Action of March 23, 2007, please consider the following. Also please charge Customer Deposit Account (50-4019) for a one-month extension of time.

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Claims** begin on page 3 of this paper.

**Remarks/Arguments** begin on page 7 of this paper.